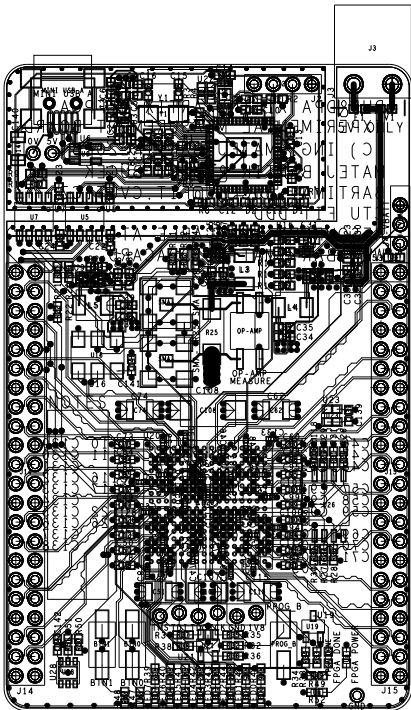
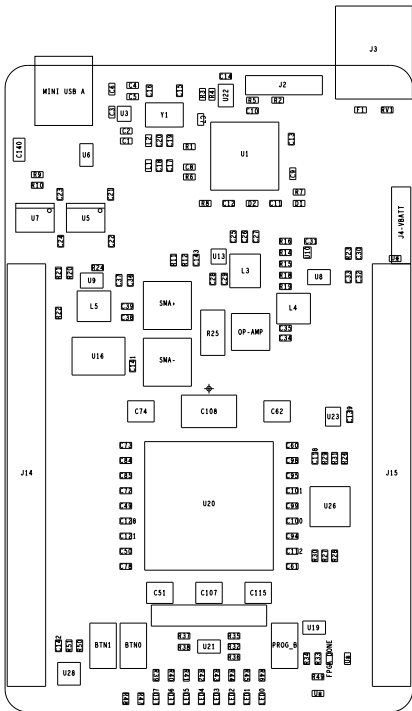


PUF&DPA XILINX ARTIX-7 FPGA
 EXPERIMENTAL MEASUREMENT BOARD
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c133 c49
 c138 c93 c46
 c116 c102 c125
 c120 c116 c125
 c120 c104 c131 c130
 c105 c91 c70 c71
 c137 c50 c132 c134 c122 c120 c116 c125
 c58 c136 c112 c104 c103 c117 c105 c91 c70 c69 c71
 c57 c59 c77 c103 c117 c105 c91 c70 c69 c71
 c82 c135 c69 c83







C46	C77	□	□	□	□	C110	C112	C9
C47	C82			□	□	C111	C113	C0
C48	C83			□	□		C114	C1
				□	□		C115	C2
C57	C91			□	□	C116	C117	C3
C58	C92	□	□	□	□		C118	C4
C59	C93			□	□	C122	C124	C5
				□	□	C126	C128	C6
C69	C102	□	□	□	□		C130	C7
C70	C103			□	□		C132	C8
C71	C104			□	□			
	C105	□	□	□	□			